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Dated: December 5, 2005

Signature: 

(Kerry G. Keenan)

Docket No.: 333772000900
(PATENT)**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:
Ankan PRAMANICK et al.

Application No.: 10/772,327

Confirmation No.: 4514

Filed: February 6, 2004

Art Unit: 2863

For: METHOD AND APPARATUS FOR TESTING
INTEGRATED CIRCUITS

Examiner: S. Kundu

AMENDMENT AFTER FINAL ACTION (37 C.F.R. SECTION 1.116)

MS AF
EXPEDITED PROCEDURE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

This is in response to the final Office Action dated October 3, 2005 (Paper No. 06022005), for which a response is due on January 3, 2006. This response is filed within two months of the issuance of the final Office Action and therefore qualifies for expedited review. Accordingly, this response is timely filed. Reconsideration and allowance of the pending claims, as amended, in light of the remarks presented herein are respectfully requested.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.

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